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** CONTINUING DATA *****

This application is a DIV of 09/379,802 08/24/1999 PAT 6,657,229
which is a DIV of 08/864,796 05/28/1997 ABN

YES TT

** FOREIGN APPLICATIONS *****

JAPAN 8-156266 05/28/1996

JAPAN 8-195437 07/05/1996

JAPAN 8-293369 10/15/1996

JAPAN 9-19860 01/17/1997

JAPAN 9-119884 05/09/1997

YES TT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/28/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	49	77	10
Examiner's Signature <i>Wain</i>	Initials TT			

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TITLE

Semiconductor device, a method of manufacturing the semiconductor device and a method of deleting
information from the semiconductor device

☐ All Fees☐ 1.16 Fees (Filing)